

FEATURES OF FIELD CONTROL STATION HARDWARE

KATO Eiji *1 NAKAMOTO Eiji *1 MATSUDA Toshihiko *1

We have developed a field control station (FCS) for the CENTUM CS 1000, a distributed control system for small- and medium-scale plants. Thus, in contrast to the FCSs of the CENTUM CS distributed control system for large-scale plants, these new FCSs are designed for small- and medium-scale plants. The main objective of their development was to make their architecture the same as the CENTUM CS's and to meet the requirements of the global market. While the CPU card, power supply card, VLnet coupler unit, and nest were newly developed, other components were either adopted from or made to be compatible with the CENTUM CS's in order to achieve the same architecture as the CENTUM CS. For example, the same input/output modules as those of the CENTUM CS are used, as well as a processor with a compatible object-oriented design and the VLnet, which has a logic sequence compatible with the CENTUM CS's Vnet. In order to make the system suitable for the global market, the hardware was made compact, low-priced, highly reliable, and easy-to-maintain as well as compliant to various international standards. The CPU card, power supply card, and VLnet coupler unit can be made dual redundant, and duplex configuration selected for each of these by the user.

INTRODUCTION

The development of the CENTUM CS 1000 distributed control system for small- and medium-scale plants was based on two basic design principles: 1) to have the same architecture as the CENTUM CS and 2) with the objective of becoming a global product. The main objectives for the development of the FCSs were compactness, high reliability and easy maintenance.

FUNCTIONS AND FEATURES OF FCSS

FCS Functions

FCSs are connected to each other and to human interface stations (HISs) and other stations and gateways via VLnet. Communication over the VLnet enables FCS databases to be downloaded and the various settings of an FCS to be set and read from an HIS. It also enables an HIS to be notified of alarms by the

FCSs. FCSs are equipped with a number of different input/output (I/O) modules that connect the various analog and digital input/output signals as well as general-purpose communication lines such as RS-232C to the FCS. The signals acquired by the I/O modules are subjected to input processing, control computation, logic operations, and then output processing before they are finally output from the FCS's I/O modules.

FCS Features

(1) Same Architecture as CENTUM CS

The FCS of the CENTUM CS 1000 has the same architecture as those of the CENTUM CS distributed control system for large-scale plants. As a result, the user can build up a system based on the same architecture for whatever scale of plant in order to best meet the needs of their plant. The compatible system architecture also means that the engineering work required for building up systems, operation, and maintenance where both the CENTUM CS and CENTUM CS 1000 systems are used, is simple due to the commonly used parts and consistent functionality.

The CENTUM CS 1000 achieves the same architecture as the CENTUM CS by utilizing its software and hardware

*1 Industrial Automation Systems Business Division

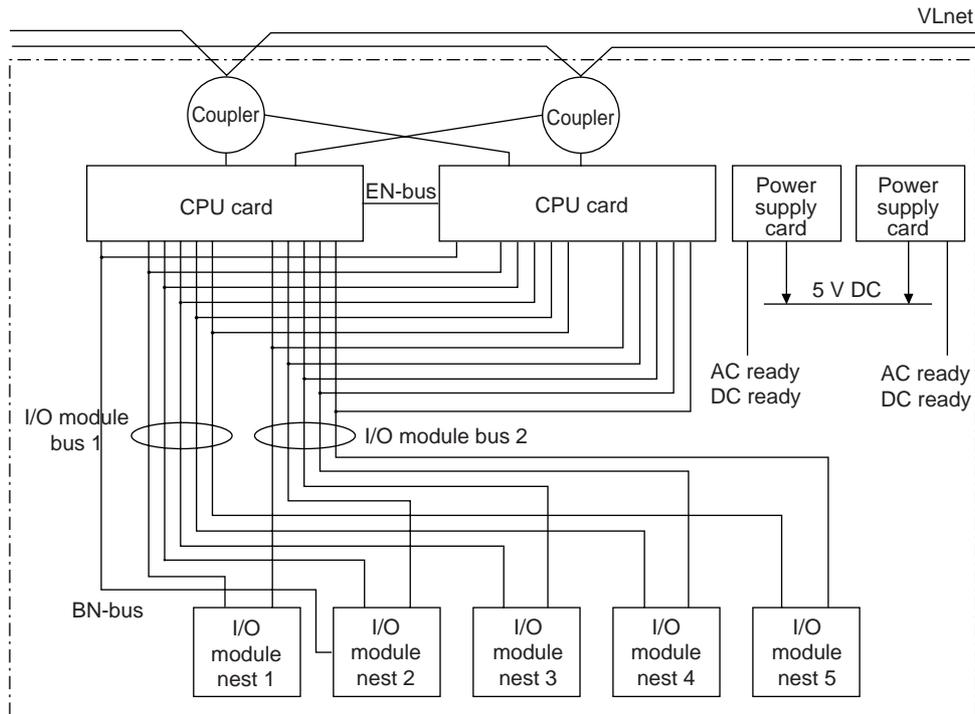


Figure 1 FCS Configuration

resources and employing:

- Processors with an object-oriented design that is compatible with that of the CENTUM CS FCSs
- VLnet control bus that is compatible with that of the Vnet (employed in CENTUM CS) in terms of logic
- The same I/O modules as those of the CENTUM CS

(2) Suitability to Global Markets

To meet the needs of the global market, the FCS was designed to be compact, low cost, highly reliable, and easy to maintain as well as to be compliant to major international safety standards and the EMC (electromagnetic compatibility) standards (for CE mark accreditation) that are required in Europe. The CENTUM CS 1000 is compliant to the following standards:

- EMC standards
 - EN55011 Class A, Group 1
 - EN50082-2
- Safety standards
 - EN61010-1
 - CSA-C22.2 No. 1010.1

In consideration of standard trends in the future, the hardware was designed and assessed so as to also comply with the following EMC standards:

- EN61000-3-2, Class A (relating to power line harmonics)
- EN61000-4-5 (relating to surge immunity)
- EN61000-4-11 (relating to voltage dip and interruption)

(3) Compact Size

To make the hardware compact, the system is distributed at the control bus level thereby omitting the remote communication I/O bus used in the CENTUM CS. Further, the CPU card, power supply card, communication interface

(VLnet interface), and I/O modules are installed in the same nest so that the CPU card can access the I/O modules directly.

(4) High Reliability

In addition to using the field-proven parts of CENTUM CS systems, each unit is made redundant for better reliability. The system is configured so that the CPU card, power supply card, VLnet interface, and input/output bus can be all made redundant, with all but the input/output bus requiring the redundancy specification to be selected by the user.

Two CPU cards run at the same time to form a synchronous hot standby system. While the system is running, the standby processor card performs control computations synchronously with the currently running processor card. Hence, control is always present even when switching between the duplexed control processors.

The power supply cards are made redundant by having the +5-V outputs from the two power supply cards pass through the diodes inside the cards and coupled on the backboard.

The VLnet interface and input/output bus have the same system of redundancy. Two bus lines are used alternately and an error check is made each time. Upon detection of an error, the problematic line is disconnected and a message raised to the user that explains how to remedy the problem.

When all units are redundant, the only non-redundant portions are the main power distribution board and the part of the switching circuit for duplexed CPU cards. This ensures high reliability.

(5) Easy Maintenance

The FCS do not need cooling fans. Therefore, FCSs do not require fans to be replaced or the fan filters to be periodically cleaned or replaced. Except for the backboard, all parts are

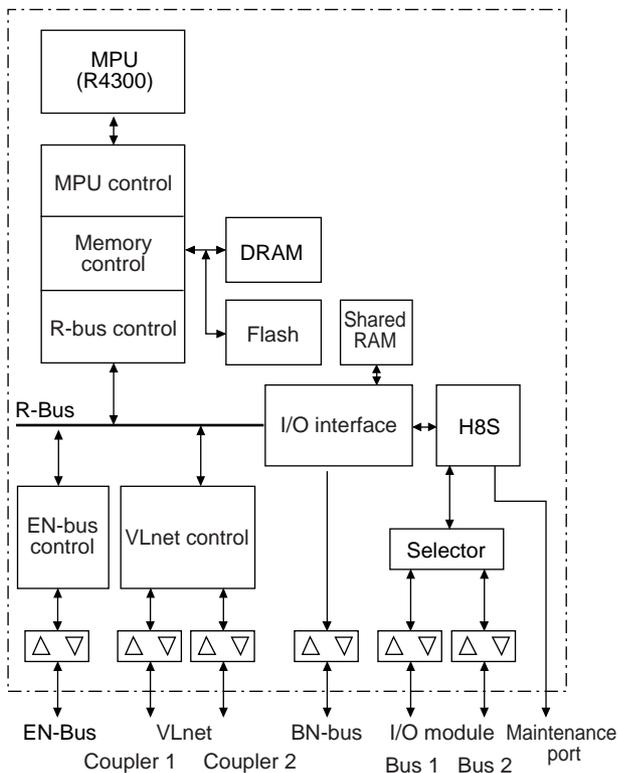


Figure 2 Internal Block Diagram of CPU Card

replaceable from the front, thus facilitating replacement of parts during maintenance. Besides this, all redundant units are hot swappable, that is, they can be replaced without turning off the power or interrupting the control.

(6) Installation of FCS

The size and mounting space of an FCS are designed such that the FCS can be mounted on a 19-inch rack in the same way as the rack mountable units of the CENTUM CS.

(7) Low Impact on Global Environment

Throughout the design phase, a great deal of consideration was given to the impact on the global environment. This contributed to the compact space-saving design, low power consumption, and the adoption of base materials that are less harmful to the environment, e.g., materials that easily decompose. The total emission of CO₂ over the life cycle from base materials through to manufacturing and finally discarding it, was calculated. The results showed that CO₂ emission can be reduced by 35% from that of the same scale conventional products.

CONFIGURATION OF FCS

As shown in Figure 1, an FCS is mainly composed of CPU cards, power supply cards, VLnet coupler units, a main nest, I/O module nests, and I/O modules. The cards can be arranged so as to meet the user's input power supply voltage and redundancy requirements. The input power supply can be selected from

among 100-120 V AC, 220-240 V AC, and 24 V DC. Regardless of the input voltage, the power supply to the inside of the FCS is regulated at +5 V DC in order to simplify the internal structure. The reduction in the power consumption of each card and the simplified configuration have lowered the total heat dissipation and made fans unnecessary.

There are several kinds of internal buses:

- EN-bus connecting the CPU cards and exchanging data between them;
- dedicated buses connecting the VLnet couplers and CPU cards;
- I/O module buses connecting five I/O module nests to the CPU cards; and a
- BN-bus connecting a general-purpose communication module nest (AMN51) to the CPU cards.

The BN-bus enables a simple communication nest to be used in the CENTUM CS 1000 to achieve the same high-speed general-purpose communication that required the CENTUM CS to be have a nest with dedicated power supply units and RIO bus communication units.

The details of each module are given below.

CPU Card

Refer to Figure 2 for the internal block diagram of a CPU card. There are two models of CPU cards: the CP701 for basic systems and the CP703 for enhanced systems. The basic system model has 8 MB of memory, while the enhanced system model has 16 MB. The model chosen depends on the type of system software used. The main memory is ensured of high reliability by the error-correction code (ECC). The R4300 was employed for the first time as the main microprocessor unit (MPU) of the CPU card. The major specifications of the R4300 are as follows:

- Internal clock: 75 MHz; external clock: 25 MHz
- Built-in 16-kilobyte instruction cache and 8-kilobyte data cache
- Supporting an address area of 4 gigabytes (GB) in the 32-bit mode
- Supporting 32-bit, 16-bit, and 8-bit data

This R4300 microprocessor unit with built-in cache and 16-megabit DRAM achieves high-speed processing at a low cost. Its low 3.3-V power supply and DRAM has decreased power consumption as well as heat dissipation. The 3.3-V self-refreshing DRAM has also allowed a smaller-capacity battery unit than those of earlier models to be used, helping to achieve the more compact design.

A flash memory is employed to store the boot program. This means that ROM need not be prepared during manufacturing and replacement of the ROM due to changes in the program as the boot program can be written into the flash memory that is mounted on the printed board.

The MPU load for accessing the I/O modules has been decreased by the following: having the H8S one-chip microcomputer access the I/O modules and structuring the data and statuses of the I/O modules in the shared RAM. The MPU can then read these data statuses from the shared RAM as necessary. This H8S one-chip microcomputer has a maintenance port, which is used

during development, preparation for shipment, inspections, and maintenance to improve the efficiency in each phase. For example, the control for writing the program to the flash memory can be run via this H8S one-chip microcomputer during maintenance.

The redundancy architecture of the CPU is referred to as a synchronous hot standby system, which is fundamentally the same as that of the CENTUM CS, the only difference being the addition of the new error detection and protection functions. These functions set write-protected areas in each CPU card to protect the program and database areas against illegal address-writing instructions from the other CPU card, and thereby prevent both cards from failing due to illegal accesses caused by malfunctions in the MPU. Other newly added functions include the memory management unit (MMU) and write-protection which ensure data integrity, the parity check of addresses and data, the ECC memory, and a two-wire signal self-checker.

Power Supply Card

The power supply card is designed to supply power to the common nests, such as the CPU cards, and up to five I/O module nests. Standardizing the output voltages to +5 V DC has simplified the circuit and structure and reduced the number of parts. This allowed a power-factor improvement unit to be built in so as to comply with the aforementioned EN61000-3-2, Class A standard (relating to power line harmonics). The +5 V DC outputs from the two power supply cards pass through diodes so that they can be coupled externally for redundancy purposes.

The input-voltage monitoring signal (AC ready) and output-voltage monitoring signal (DC ready) together with the guaranteed retention time of the +5-V DC output, enable control to continue over a temporary power failure. The output-voltage retention time immediately after a power failure is clearly defined in the specifications since it is closely related to the software-saving process in the CPU card.

In order to meet the needs of global markets, three models have been prepared to cater for differing input power supply voltages: namely, a 100-120 V AC input model, a 220-240 V AC input model, and a 24 V DC input model.

Control Bus

The new VLnet control bus uses 10BASE2 cables and connectors (50-Ω coaxial cables and BNC coaxial connectors) in order to reduce costs and simplify the instrumentation. It is a token-passing bus with a logic sequence compatible with the Vnet used in the CENTUM CS. The major specifications are as follows:

- Transmission speed: 10 Mbps
- Maximum number of stations on a bus:
24 (HISs ≤ 8, FCSs ≤ 16)
- Maximum transmission distance: 185 m/segment (can be extended to 20 km when a fiber optic repeater is used)

The connector unit used in the VLnet coupler was designed to be interchangeable with the connector unit for the Vnet. Hence, these new FCS models will be able to be connected to the Vnet and added to the lineup of CENTUM CS system components, by

simply changing the connector units to those for the Vnet.

Nest

The FCS nest is composed of VLnet couplers, battery units for backing up the CPUs' main memory, a backboard, a power distribution board, and a ready signal output unit in addition to the CPU cards and power supply cards. A standard FCS model has two I/O module nests. An optional version having five I/O module nests is also available.

Input/Output Modules

The CENTUM CS 1000 is designed such that it can use the same I/O module nests and modules developed for the CENTUM CS. Besides these existing components, a new communication nest was developed for installing the ACM21 and ACM22 high-speed communication modules (which were previously developed for the CENTUM CS), the AAM10 and AAM50 I/O modules for specialized applications were developed to achieve an economical system configuration. The key points in the development of the AAM10 and AAM50 are:

- Functions are integrated in the newly developed gate array, and the microprocessor, A/D conversion IC, and other parts used in the AAM11 and AAM51 modules are removed.
- A double-layer board is adopted for the printed circuit board due to the simplified circuit and reduced number of parts.
- Parts are carefully assessed and chosen in consideration of overseas production capabilities.

MAINTAINABILITY AND RELIABILITY

To achieve a high degree of reliability in spite of the short development period, the parts and circuits field-proven in the CENTUM CS system have been used as far as possible and sufficiently reviewed and assessed in terms of their temperature derating. By using the same I/O modules and module nests as the CENTUM CS, the same level of the reliability as the CENTUM CS can be assured for the very first batch of deliveries. In regard to maintainability, this system configuration eliminates the need for users to have so many different spare parts and learn the different maintenance procedures, thus reducing the maintenance costs.

CONCLUDING REMARKS

The development of the CENTUM CS 1000 was accomplished in a short period of time by using the parts and technologies field-proven in the CENTUM CS system wherever possible. It is a low-end model in the CENTUM CS series and shall act as a key product for the global market-taking over from the μXL systems. The CENTUM series hardware has identical architecture for plants of all scales. We shall continue to pursue cost reductions, increased system connectivity, and compliance with new standards in accordance with the demands of the market. The CENTUM CS 1000 must be assessed and updated so as to meet the ever-increasing market needs. ◆